

ABSTRACT OF THE DISCLOSURE

A backlight inverter for an LCD panel of an asynchronous pulse width modulation (PWM) driving type which is capable of 5 driving a plurality of cold cathode fluorescent lamps (CCFLs) in pairs and controlling a plurality of PWM drive signals for respective operations of the lamps to make the phases thereof different. The backlight inverter comprises a main driving integrated circuit (IC) for generating first and second PWM 10 pulses in response to a dimming voltage based on a brightness control and an internally generated PWM oscillation signal, delaying the generated first and second PWM pulses by a predetermined period of time and outputting first and second PWM drive signals on the basis of the delayed first and second 15 PWM pulses, respectively, at least one sub-driving IC for secondarily delaying the delayed first and second PWM pulses from the main driving IC by the predetermined period of time and outputting third and fourth PWM drive signals on the basis of the secondarily delayed first and second PWM pulses, 20 respectively, and a plurality of lamp operating circuits for operating the pairs of lamps in response to the first and second PWM drive signals and the third and fourth PWM drive signals, respectively. The lamps have different PWM on/off periods so that overshoot of a power supply circuit can be 25 reduced so as to keep the entire system power stable.